

DANAMRAJ & YOUST, P.C.

ATTORNEYS AND COUNSELORS
INTELLECTUAL PROPERTY LAW AND RELATED MATTERS

SHREEN K. DANAMRAJ
LAWRENCE R. YOUST
BRANDI W. SARFATIS
KENNETH T. EMANUELSON

PREMIER PLACE, SUITE 1450
5910 NORTH CENTRAL EXPRESSWAY
DALLAS, TEXAS 75208
(214) 750-5666 (tel)
(214) 363-8177 (fax)

Direct Dial: (214) 363-4266
lawrence@dy-iplaw.com

January 21, 2005

Via Facsimile 703.872.9306

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Re: Serial No.: 10/771,796
Method for Constructing a Wafer-Interposer Assembly
Examiner: William D. Coleman
Art Unit: 2823
Confirmation No.: 3342
Our File: 1384-1031

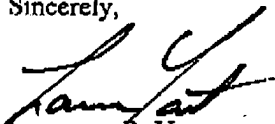
Dear Sir:

Enclosed for filing please find the following items (13 + 1 pages) relating to the above-identified application:

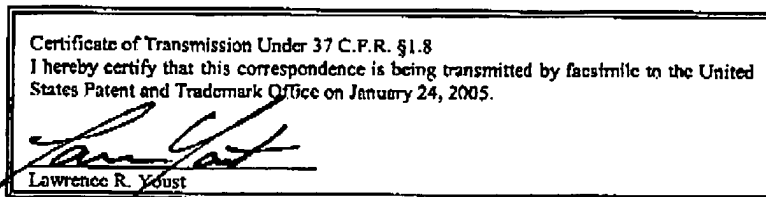
- (1) Response Under 37 C.F.R. §1.111 (11 pages);
- (2) Terminal Disclaimer (1 page); and
- (3) Form PTO-2038 (1 page).

If you have any questions or comments concerning this matter, please call the undersigned at your earliest convenience. Otherwise, please accept the enclosed.

Sincerely,


Lawrence R. Youst
Reg. No. 38,795

Enclosures



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Attorney Docket No.: 1384-1031

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In Re Application: Jerry D. Kline

Serial No.: 10/771,796

Filed: February 2, 2004

Art Unit: 2823

Confirmation No.: 3342

Examiner: William D. Coleman

For: Method for Constructing a Wafer-Interposer
AssemblyCommissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450Response Under 37 C.F.R. §1.111

Dear Sir:

This is a response to the Office Action dated January 19, 2005 due for reply by April 19, 2005. In view of the following remarks, reconsideration and withdrawal of the outstanding rejections and allowance of the claims pending in this application are respectfully requested.